displays and configuration properties of a computing system, the coupling controller determining whether a current configuration of the multiple displays to the computing system can be reconfigured such that the display preferences can be fulfilled while maintaining effective configuration of the current configuration when the display preferences cannot be fulfilled; (c) configure the computing system and the multiple displays in accordance with the display preferences when the display preferences can be fulfilled, and reconfigure operable coupling of the multiple displays to the computing system such that the multiple displays are configured in accordance with the display preferences when the current configuration can be reconfigured; (d) operably couple a display controller of the computing system to the multiple displays, the display controller providing display data to the multiple displays; (e) operably couple the display controller to a plurality of screen memories, each of the plurality of the screen memories storing separate display data and the display controller retrieving the display data from the plurality of screen memories; and (f) operably couple the display controller to a plurality of display drivers, each of the plurality of display drivers writing the separate display data to the plurality of screen memories.

29. (Amended) The video graphics processing circuit of claim 24, wherein the memory further comprises programming instructions that cause the processing unit to operably couple a first display controller of the computing system to a first display of the at least one of the multiple displays and operably coupling a second display controller of the computing system to a second display of the multiple displays.

30. (Amended) The video graphics processing circuit of claim 29, wherein the memory further comprises programming instructions that cause the processing unit to operably couple the first display controller to a third display of the multiple displays.

31. (Amended) The video graphics processing circuit of claim 24, wherein the memory further comprises programming instructions that cause the processing unit to operably couple a first display controller of the computing system to a first display of the multiple displays,

operably coupling a second display controller of the computing system to a second display of the multiple displays, and operably coupling the first and second display controllers to one of the screen memory.

33. (Four Times Amended) A digital storage medium for storing programming instructions that, when executed by a processing unit, cause the processing unit to configure multiple displays associated with a computing system, the digital storage medium comprises:

first means for storing programming instructions that cause a coupling controller of the processing unit to receive display preferences regarding the multiple displays;

second means for storing programming instructions that cause the coupling controller of the processing unit to determine whether the display preferences can be fulfilled in observance of at least one of: configuration properties of the multiple displays and configuration properties of the computing system;

third means for storing programming instructions that cause the coupling controller of the processing unit to configure the computing system and the multiple displays in accordance with the display preferences when the display preferences can be fulfilled;

fourth means for storing programming instructions that cause the processing unit to:

determine whether a current configuration of the multiple displays to the computing system can be reconfigured such that the display preferences can be fulfilled while maintaining effective configuration of the current configuration when the display preferences cannot be fulfilled;

reconfigure operable coupling of the multiple displays to the computing system such that the multiple displays are configured in accordance with the display preferences when the current configuration can be reconfigured;

operably couple a display controller of the computing system to the multiple displays, the display controller providing display data to the multiple displays;

operably couple the display controller to at a plurality of screen memories, each of the plurality of the screen memories storing separate display data and the

display controller retrieving the display data from the at least one of the plurality of screen memories; and

operably couple the display controller to a plurality of display drivers, each of the plurality of display drivers writing the separate display data to the plurality of screen memories.

38. (Amended) The digital storage medium of claim 33 further comprises means for storing programming instructions that cause the processing unit to operably couple a first display controller of the computing system to a first display of the multiple displays and operably coupling a second display controller of the computing system to a second display of the multiple displays.

39. (Amended) The digital storage medium of claim 38 further comprises means for storing programming instructions that cause the processing unit to operably couple the first display controller to a third display of the multiple displays.

40. (Amended) The digital storage medium of claim 33 further comprises means for storing programming instructions that cause the processing unit to operably couple a first display controller of the computing system to a first display of the multiple displays, operably coupling a second display controller of the computing system to a second display of the multiple displays, and operably coupling the first and second display controllers to a screen memory.

42. (Amended) A video graphics processing circuit for displaying at least one image on a plurality of displays, comprising:

a plurality of display controllers included on a single video graphics card;

a plurality of drivers;

memory, wherein at least a portion of the memory is screen memory, the screen memory having a plurality of screen memory portions, each of the plurality of screen memory portions storing separate display data;

coupling module operably coupled to a plurality of displays and the screen memory; and

a coupling controller operably coupled to receive display preferences and to determine whether the display preferences can be fulfilled in observance of configuration properties, the display preferences including at least one of displaying an image on more than one of the displays, displaying separate images on each of the displays, displaying a portion of the image on one of the displays and displaying the image on another one of the multiple displays, providing different refresh rates for at least two of the displays, providing different resolutions for at least two of the displays, selecting one of the displays to display a predetermined type of image, and displaying a first portion of the image on a first one of the displays and displaying a second portion of the image on a second one of the displays;

wherein, when the display preferences can be fulfilled, the coupling controller provides configuration requirements to the coupling module, wherein the coupling module, based on the configuration requirements, operably couples at least one of the plurality of display controllers with at least a portion of the screen memory and with at least one display, a respective display driver of the plurality of display drivers thereby writing respective separate display data to a respective one of the plurality of screen memory portions, and wherein the at least one of the plurality of display controllers retrieves display data from the at least a portion of the screen memory and provides the display data to the at least one display, and wherein the coupling controller provides reconfiguration requirements to the coupling module when the display preferences cannot be fulfilled but a current configuration of the plurality of display controllers to the at least one display can be reconfigured such that the display preferences can be fulfilled while maintaining effective configuration of the current configuration.

46. (Amended) The video graphics processing circuit of claim 42, wherein the configuration requirements cause the coupling module to operably couple a first display controller of the plurality of display controllers to a first display and operably couple a second display controller of the plurality of display controllers to a second display.

47. (Amended) The video graphics processing circuit of claim 46, wherein the configuration requirements cause the coupling module to operably couple the first display controller to a third display.

48. (Amended) The video graphics processing circuit of claim 42, wherein the configuration requirements cause the coupling module to operably couple a first display controller of the plurality of display controllers to a first display, operably coupling a second display controller of the plurality of display controllers to a second display, and operably coupling the first and second display controllers to the screen memory.

50. (Added 1/13/03) The apparatus of claim 49 wherein the configuration properties comprise means for causing the coupling controller to couple a first screen memory portion to more than one of the plurality of display controllers.